



PTO/SB/122 (04-05)
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| Application Number | 10/665,927 |
| Filing Date | September 17, 2003 |
| First Named Inventor | Alexander Tetelbaum |
| Art Unit | 2825 |
| Examiner Name | Doan, Nghia M. |
| Attorney Docket Number | 02-5799 / 81604 |

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Typed or Printed
Name Timothy Croll, Reg. No. 36,771

Date

20 APR 06

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PTO/SB/86 (08-03)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: LSI Logic CorporationApplication No./Patent No.: 10/665,927 Filed/Issue Date: September 17, 2003Entitled: Method of Noise Analysis And Correction Of Noise Violations For An Integrated Circuit Design

LSI LOGIC CORPORATION, a CORPORATION
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

20 APR 06Timothy Croll, Reg. No. 36,771DateTyped or printed name(408) 433-7191SignatureTelephone numberSr. Corporate Counsel - IPTitle

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